



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of)
Hirofumi Totsuka et al.) Group Art Unit: 2616
Application No.: 10/081,234) Examiner: ANDREW CHUNG CHEUNG LEE
Filed: February 25, 2002) Confirmation No.: 3853
For: AN OPTICAL TRANSCEIVER, A)
MULTIPLEXING INTEGRATED)
CIRCUIT, A DEMULTIPLEXING)
INTEGRATED CIRCUIT, AN INTEGRAL)
MULTIPLEXING/DEMULTIPLEXING)
INTEGRATED CIRCUIT, AND A)
METHOD FOR EVALUATING AND)
TESTING THE OPTICAL)
TRANSCEIVER)

STATUS INQUIRY

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Date: May 3, 2006

Sir:

Please advise, in writing, as to the current status of the above-captioned application.

Respectfully submitted,

BUCHANAN INGERSOLL PC

Date: May 3, 2006

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